

IN THE CLAIMS:

Please amend the claims as follows:

1 (Currently Amended): A liquid crystal display panel, comprising:

a first substrate having an image display portion thereon;

a second substrate; and

a seal pattern disposed along an outer periphery of the image display portion to attach the first substrate with the second substrate,

wherein the first substrate includes:

at least one conductive line disposed at a corner portion of the first substrate,

a multi-layer film disposed on the first substrate and the at least one conductive line, the multi-layer film defining a plurality of bonding holes disposed along a region where the seal pattern is disposed, and

a transparent electrode formed on the multi-layer film and in the bonding holes such that the transparent electrode is entirely disposed within a boundary defined by an outer side of the seal pattern, at least one of the bonding holes being substantially completely filled by the transparent electrode and the seal pattern.

2 (Original): The liquid crystal display panel according to claim 1, wherein the first substrate and the second substrate are attached such that one shorter side and one longer side of the first substrate protrude from a boundary defined by the second substrate.

3 (Original): The liquid crystal display panel according to claim 2, wherein a gate pad portion is disposed at the protruded shorter side of the first substrate and a data pad portion is disposed at the protruded longer side of the first substrate.

4 (Original): The liquid crystal display panel according to claim 2, wherein the at least one conductive line includes at least one line-on-glass line disposed at a corner portion of the first substrate where the shorter side and the longer side meet.

5 (Original): The liquid crystal display panel according to claim 1, wherein the at least one conductive line transmits DC signals including a gate high voltage (V_{gh}), a gate low voltage (V_{gl}), a common voltage (V_{com}), a ground voltage (GND) and a power supply voltage (V_{cc}), and AC signals including a gate start pulse (GSP), a gate shift clock (GSC) and a gate enable signal (GOE).

6 (Original): The liquid crystal display panel according to claim 1, wherein the multi-layer film includes a gate insulation film, a semiconductor layer, and a passivation film.

7 (Original): The liquid crystal display panel according to claim 6, wherein the passivation film includes at least one of a benzocyclobutene (BCB), a spin-on-glass (SOG), and a photo-acryl.

8 (Original): The liquid crystal display panel according to claim 1, wherein the second substrate includes:

a black matrix defining outer edges of pixels of the image display portion;
a color filter disposed corresponding to the unit pixel of the image display portion; and
a common electrode disposed at the upper surface of the second substrate having the black matrix and the color filter.

9 (Original): The liquid crystal display panel according to claim 8, wherein the common electrode is disposed within the image display portion and covered by the seal pattern.

10 (Original): The liquid crystal display panel according to claim 1, wherein the bonding holes are disposed over the at least one conductive line.

11 (Currently Amended): A method for fabricating liquid crystal display panel, comprising the steps of:

preparing a first substrate as a thin film transistor substrate, the first substrate including a multi-layer film on the first substrate and the at least one conductive line with the multi-layer film defining a plurality of bonding holes;

preparing a second substrate as a color filter substrate;
forming at least one transparent electrode on at least one of the first substrate and the second substrate; and

forming a seal pattern to attach the first substrate with the second substrate,

wherein the transparent electrode is entirely disposed within a boundary defined by an outer side of the seal pattern, and wherein at least one of the bonding holes are substantially completely filled by the transparent electrode and the seal pattern.

12 (Original): The method according to claim 11, wherein the first substrate and the second substrate are attached such that one shorter side and one longer side of the first substrate protrude from a boundary defined by the second substrate.

13 (Original): The method according to claim 12, wherein a gate pad portion is disposed at the protruded shorter side of the first substrate and a data pad portion is disposed at the protruded longer side of the first substrate.

14 (Original): The method according to claim 12, wherein the at least one conductive line includes at least one line-on-glass line disposed at a corner portion of the first substrate where the shorter side and the longer side meet.

15 (Original): The method according to claim 11, wherein the at least one conductive line transmits DC signals including a gate high voltage (V_{gh}), a gate low voltage (V_{gl}), a common voltage (V_{com}), a ground voltage (GND) and a power supply voltage (V_{cc}), and AC signals including a gate start pulse (GSP), a gate shift clock (GSC) and a gate enable signal (GOE).

16 (Cancelled).

17 (Currently Amended): The method according to claim ~~[[16]]~~ 11, wherein the multi-layer film includes a gate insulation film, a semiconductor layer, and a passivation film.

18 (Original): The method according to claim 17, wherein the passivation film includes at least one of a benzocyclobutene (BCB), a spin-on-glass (SOG), and a photo-acryl.

19 (Original): The method according to claim 11, wherein the second substrate includes:
a black matrix defining outer edges of pixels of an image display portion;
a color filter disposed corresponding to the unit pixel of the image display portion; and
a common electrode disposed at a surface of the second substrate having the black matrix and the color filter.

20 (Original): The method according to claim 19, wherein the common electrode is disposed within the image display portion and covered by the seal pattern.

21 (Currently Amended): A liquid crystal display panel, comprising:

a first substrate;

a second substrate;

a seal pattern disposed along an outer edge of an image display portion of the first substrate and the second substrate, and attaching the first and second substrates;

at least one line-on-glass line disposed at a corner portion of the first substrate;

a multi-layer film disposed on the first substrate with the line-on-glass line formed thereon and having a plurality of bonding holes disposed regularly along the region where the seal pattern passes; and

a transparent electrode disposed on the multi-layer film having a plurality of the bonding holes overlapping the at least one line-on-glass line and patterned not to be protruded from the seal pattern, at least one of the bonding holes being substantially completely filled by the transparent electrode and the seal pattern.

22 (Currently Amended): A liquid crystal display panel, comprising:

a first substrate having an image display portion thereon;

a second substrate; and

a seal pattern disposed along an outer periphery of the image display portion to attach the first substrate with the second substrate,

wherein at least one of the first substrate and the second substrate includes a transparent electrode such that the transparent electrode is entirely disposed within a boundary defined by an outer side of the seal pattern, and wherein at least one of the bonding holes are substantially completely filled by the transparent electrode and the seal pattern.

23 (Original): The liquid crystal display panel according to claim 22, wherein the transparent electrode defines a pixel electrode.

24 (Original): The liquid crystal display panel according to claim 22, wherein the transparent electrode defines a common electrode.